

Figure 1a
 (Prior Art)

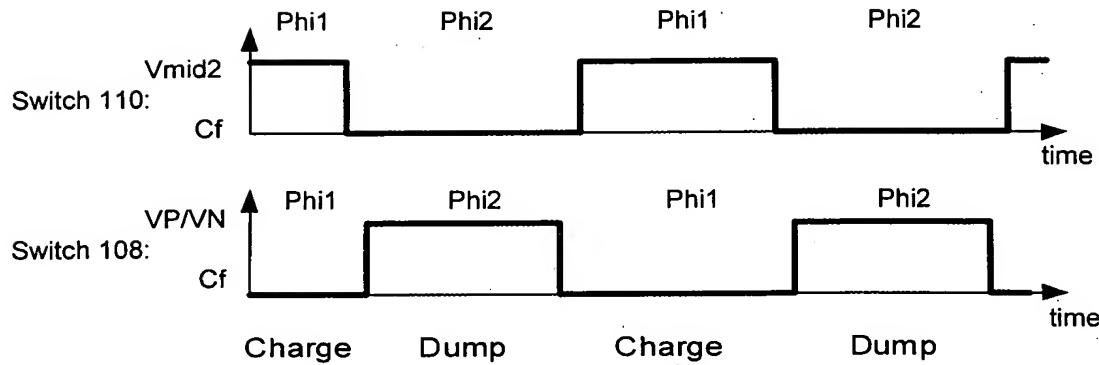


Figure 1b

	Connected to:	
Switch	During Phi1 (Charge)	During Phi2 (Dump)
110	V_{mid2}	C_f
108	V_P/VN	C_f

Figure 1c

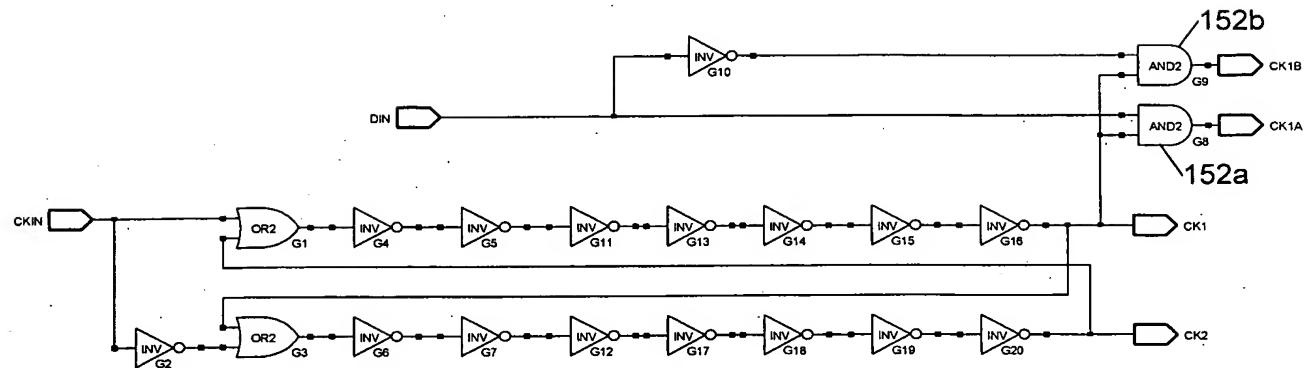


Figure 1d

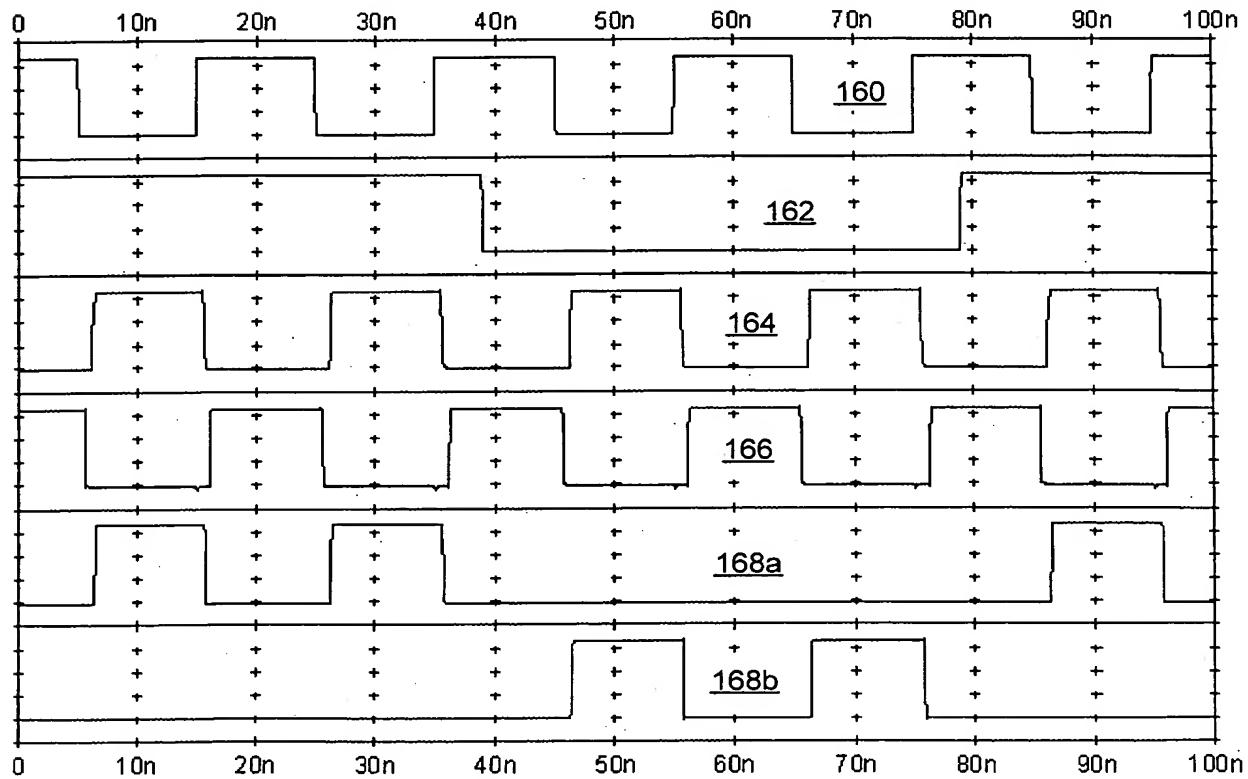


Figure 1e

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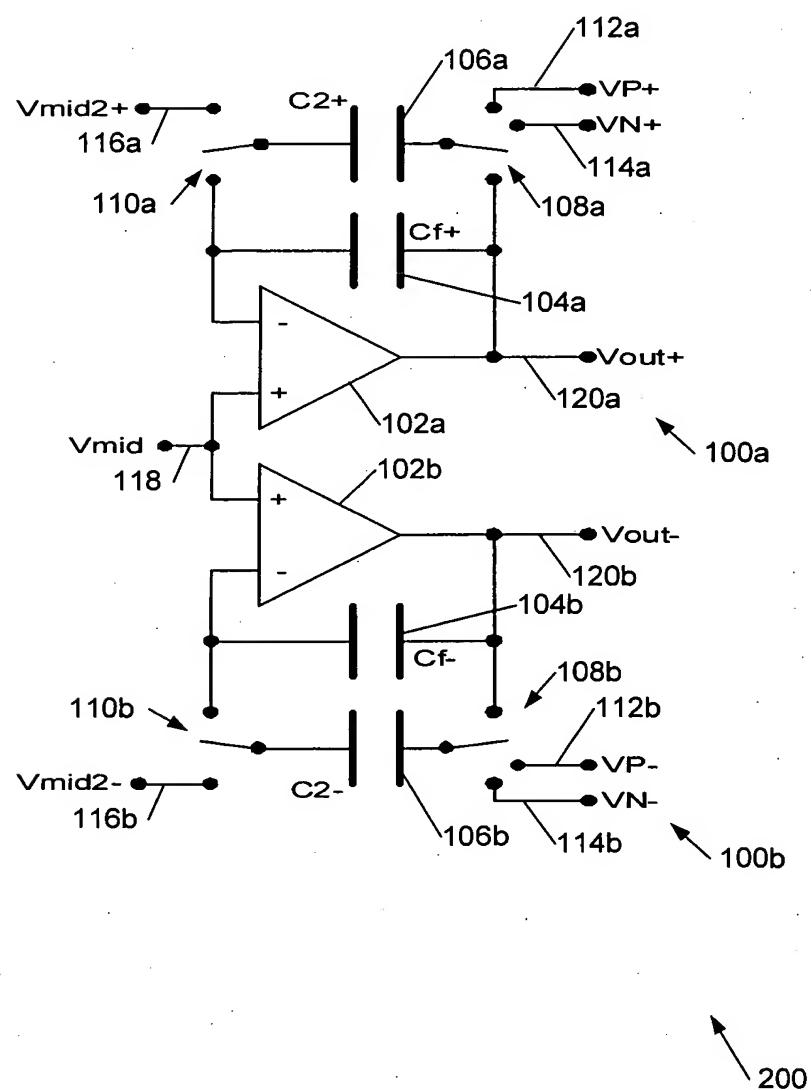


Figure 2
(Prior Art)

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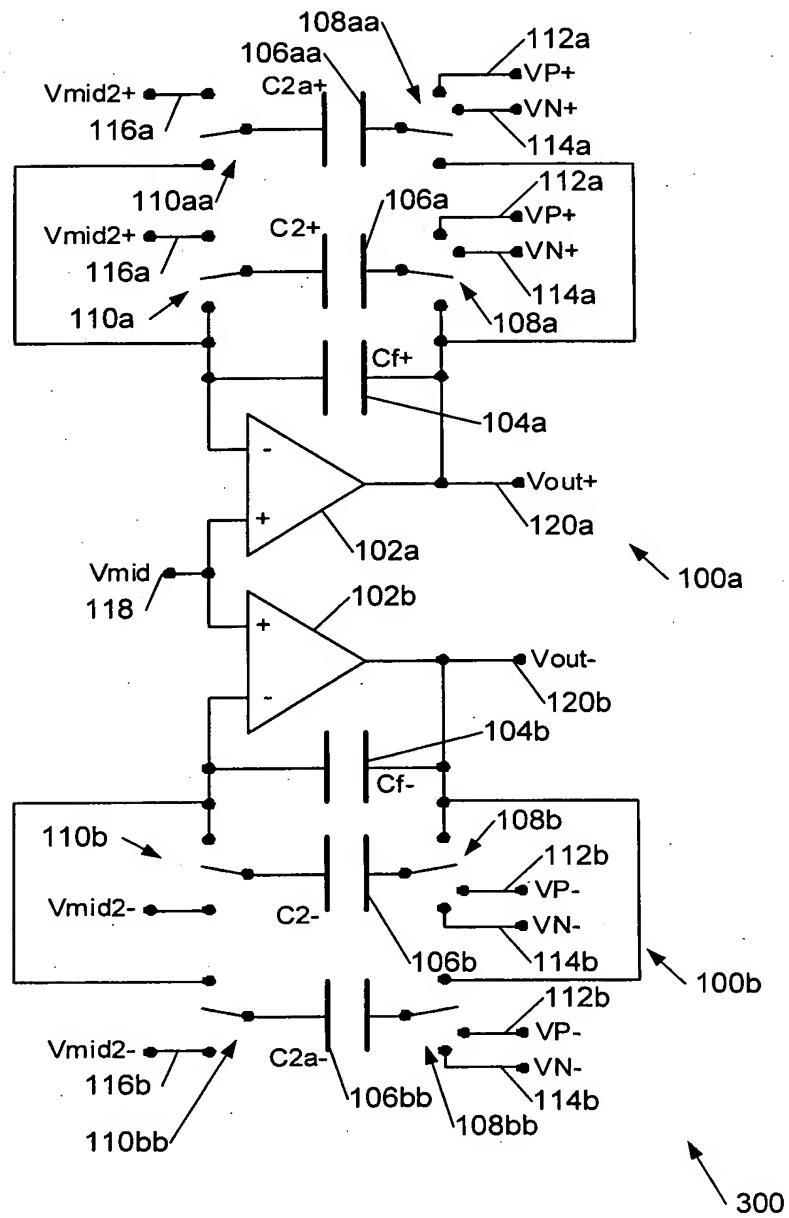


Figure 3
 (Prior Art)

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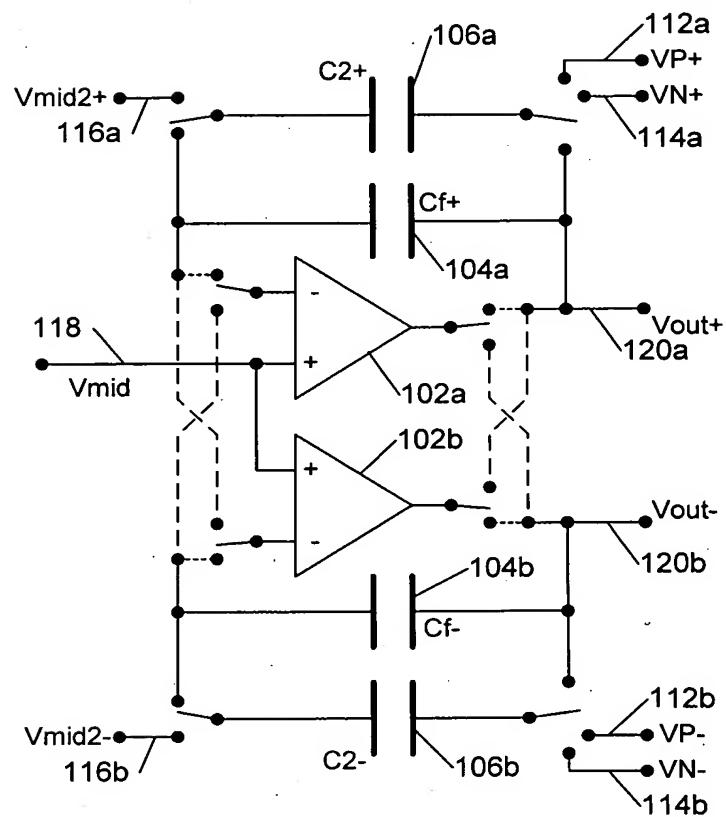


Figure 4

400

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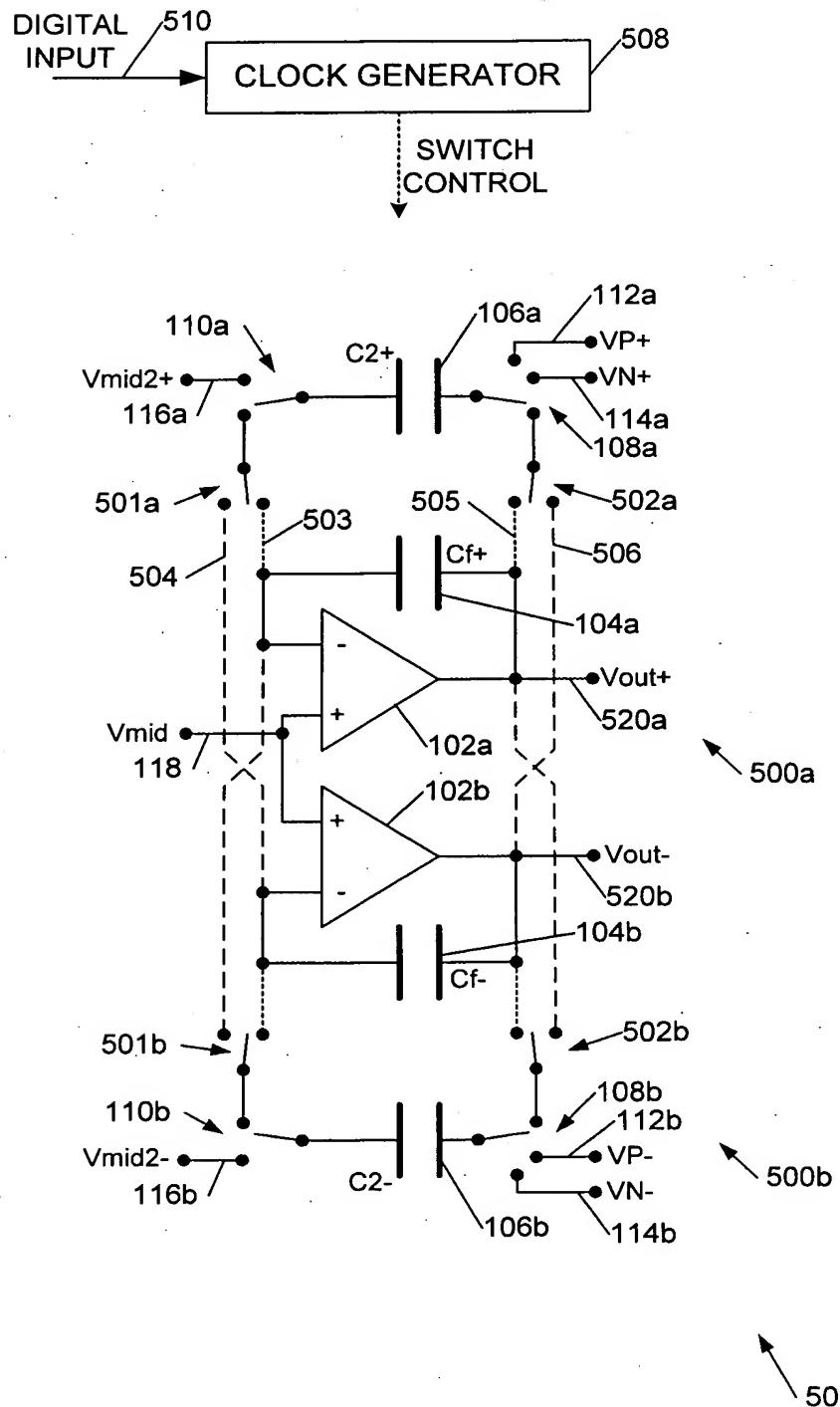
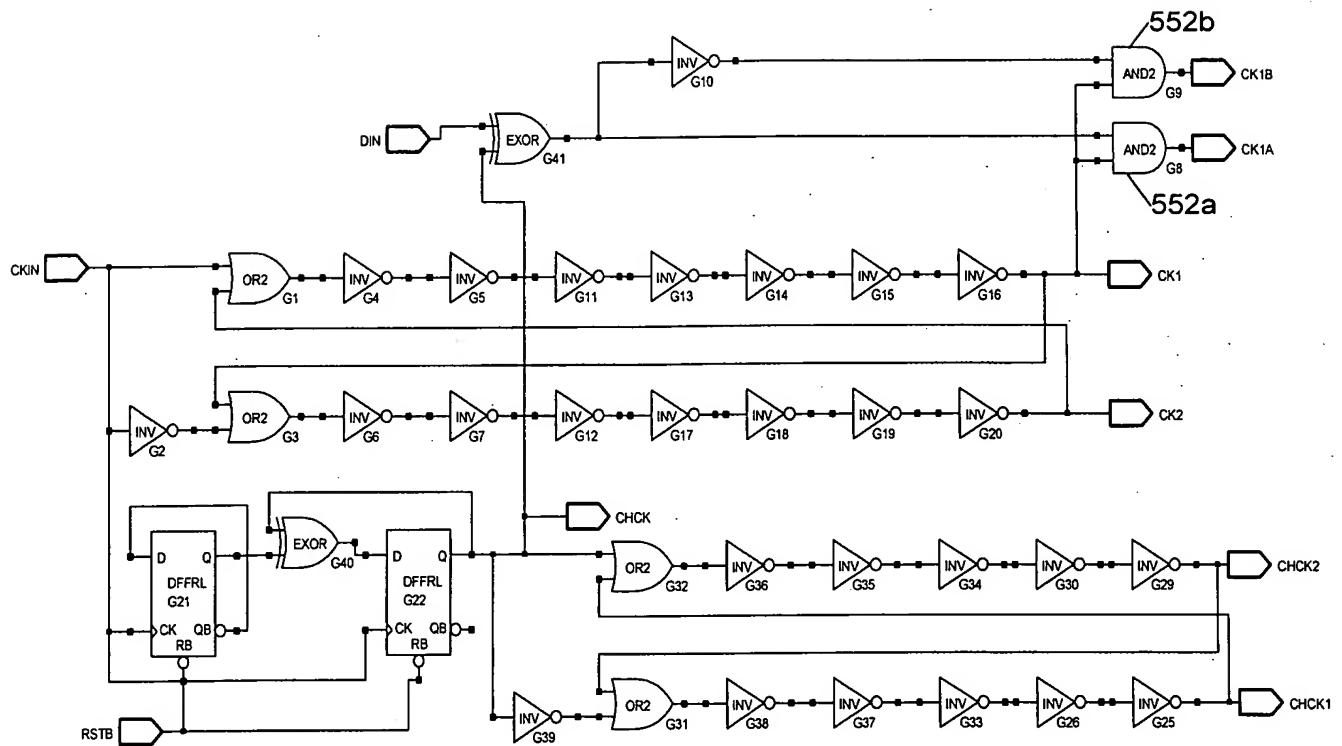


Figure 5a

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150

Figure 5b

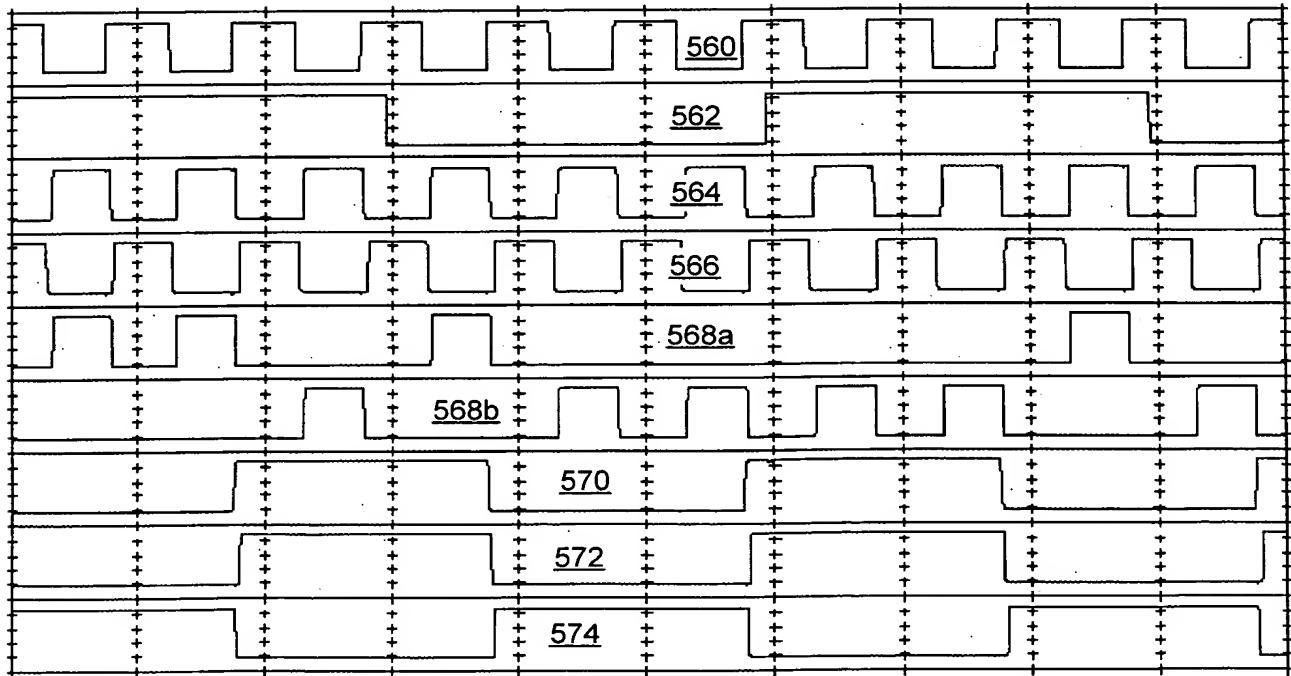


Figure 5c

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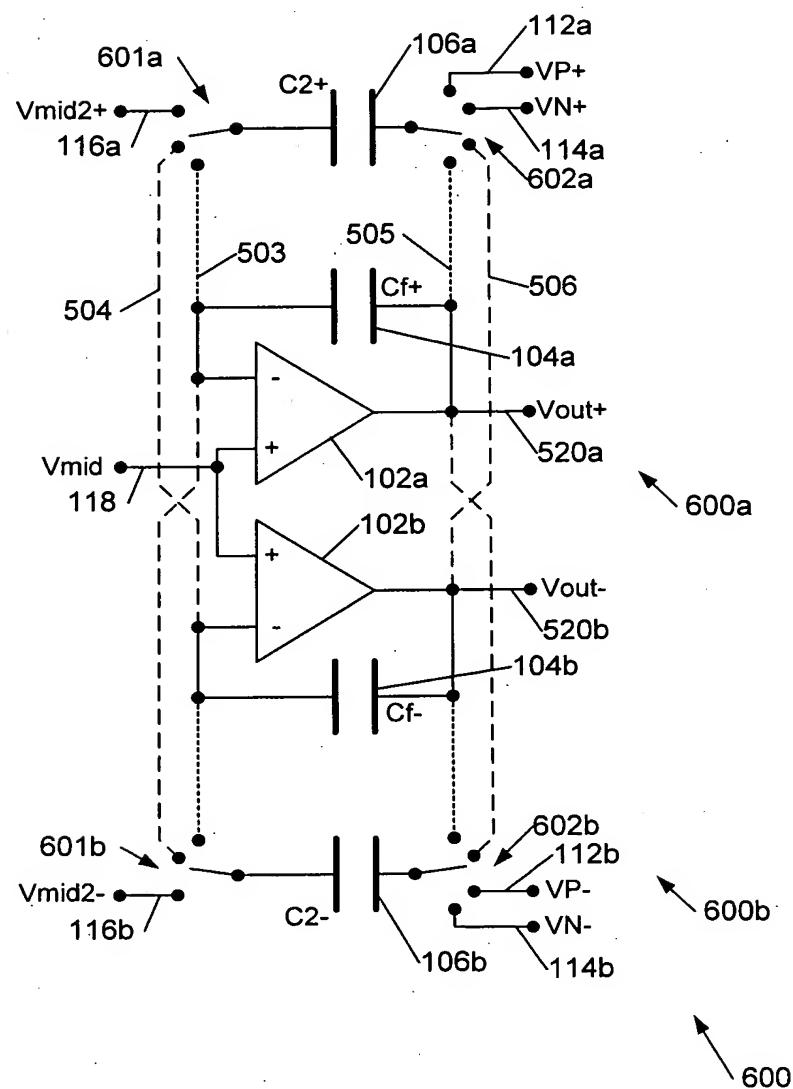


Figure 6

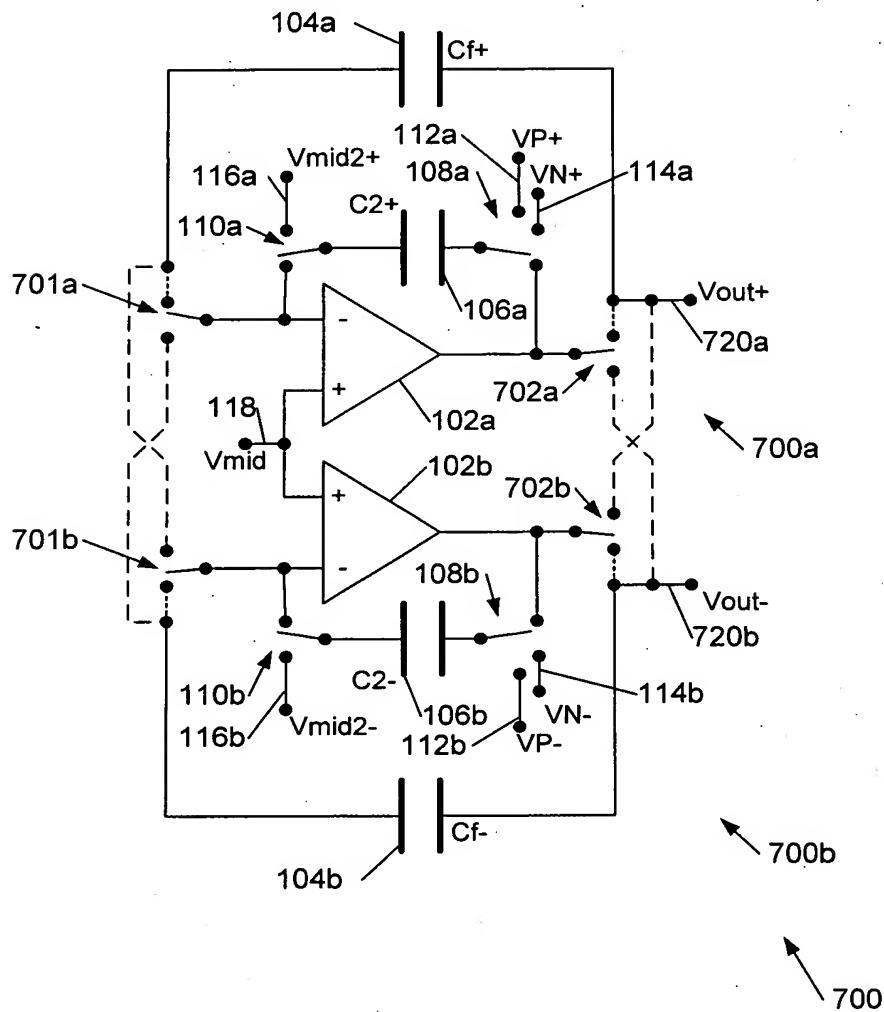


Figure 7

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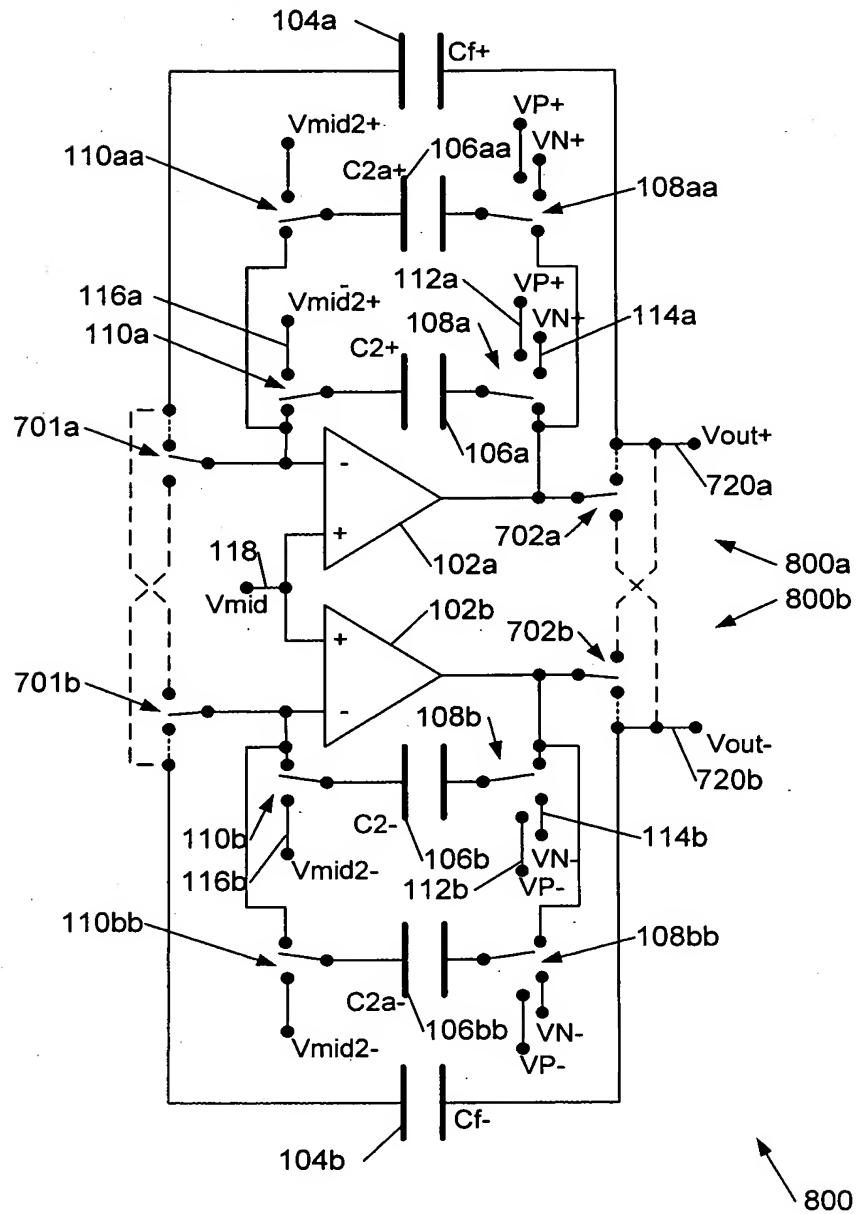


Figure 8